

520.41003X00  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s) : KIMURA et al.  
Serial No. : Not yet assigned  
Filed: On even date  
For: POLYCRYSTALLINE SEMICONDUCTOR THIN FILM  
SUBSTRATE, MANUFACTURING METHOD OF THE  
SAME, SEMICONDUCTOR DEVICE AND ELECTRONIC  
DEVICE

**INFORMATION DISCLOSURE STATEMENT**  
UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

December 28, 2001

Sir:

In the matter of the above-identified application,  
applicants are submitting herewith a copy of a communication  
from a foreign patent office in a counterpart foreign  
application and copies of the documents listed in the attached  
form equivalent to Form PTO-1449 for the Examiner's  
consideration.

This information disclosure statement is being submitted  
with the new application.

To the extent the documents listed on the attached form  
equivalent to Form PTO-1449 are not in the English language,  
the requirement of 37 CFR 1.98(a)(3) for a concise explanation  
of the relevance is satisfied by the discussion of these  
documents in the specification, for example, on page 2.

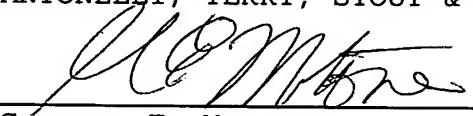
10/019407  
JC13 Rec'd PCT/PTO 28 DEC 2001

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (520.41003X00) please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

  
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Sheet 1 of 1

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DKT. NO. 520.41003X00	SERIAL NO. Not yet assigned
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)			<b>APPLICANT</b> KIMURA et al.		
			FILING DATE Dec. 28, 2001	GROUP	

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation /Abstract	
						Yes	No
AL							
AM							
AN							
AO							

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

AP	SID 96 Digest, pp. 17-20		
AQ	IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996, pp. 1454-1458		
AR	Appl. Phys. Lett. 63(14), 4 October 1993, pp. 1969-1971		
AS	Proceedings of the Institute of Electronics, Information and Communication Engineers C-II Vol. J76-C-II, 1993, pp. 241-248		
Examiner	Date Considered		